



SPECIFICATION FOR APPROVAL

()	Preliminary	Specification
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Title

1100	10:0 118 11 1 20	
Customer	SUPPLIER	LG Display Co., Ltd.
MODEL	*MODEL	LP156WH3
	 Suffix	TLSA

^{*}When you obtain standard approval, please use the above model name without suffix

15 6" HD TFT I CD

APPROVED BY	SIGNATURE
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APPROVED BY	SIGNATURE
N. J. Seong / Manager	
REVIEWED BY	
S. S. Han / Manager	
PREPARED BY	- <u></u>
Y. G. Jeon / Engineer	
Y. G. Jeon / Engineer C. Y. Jeong / Engineer	

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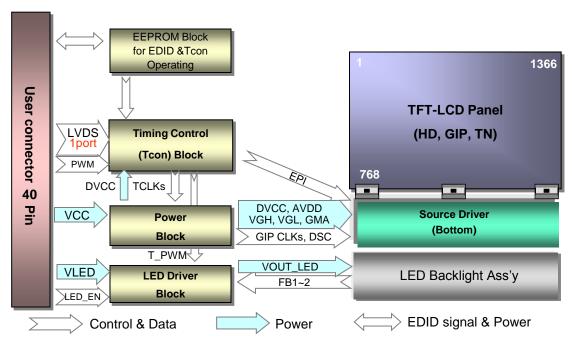
RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
0.0	May 20. 2012	-	First Draft (Preliminary Specification)	
0.1	Jul 03. 2012	11 21	Add appendix about timing Add detail Label Information	0.2
		27-29 6 –7	Update EDID Update Power Consumption	
0.2	Aug 04. 2012	12 27-29	Change Dclk to meet AMD vertical blanking time Update EDID	0.3
0.3	Sep 03. 2012	6	Update Logic Power Consumption	
1.0	Sep 26. 2012	-	Final Specification	1.0
[



1. General Description

The LP156WH3 is a Color Active Matrix Liquid Crystal Display with an integral LED backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 15.6 inches diagonally measured active display area with HD resolution (1366 horizontal by 768 vertical pixel array). Each pixel is divided into Red, Green and Blue subpixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors. The LP156WH3 has been designed to apply the interface method that enables low power, high speed, low EMI. The LP156WH3 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the subpixels, the LP156WH3 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	15.6 inches diagonal
Outline Dimension	359.5(H, Typ.) × 217.2(V, Typ.) × 3.8(D, Max.) [mm] (with PCB Board)
Pixel Pitch	0.252mm X 0.252 mm
Pixel Format	1366 horiz. by 768 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	200 cd/m ² (Typ.)
Power Consumption	Total : 3.3W (Typ.) Logic : 0.8W (Typ.@ Mosaic), B/L : 2.5W (Typ.@ VLED 12V)
Weight	400g (Max.)
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Glare treatment (3H) of the front Polarizer
RoHS Compliance	Yes
BFR/PVC/As Free	Yes for all



2. Absolute Maximum Ratings

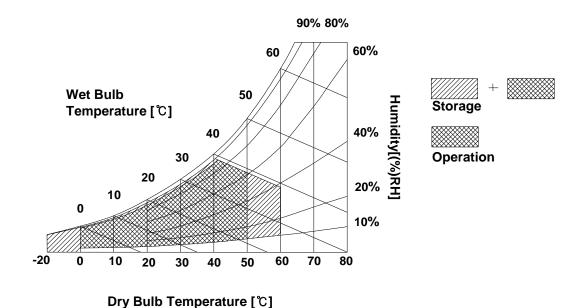
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes	
Farameter	Syllibol	Min	Max	Offics		
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.





3. Electrical Specifications

3-1. Electrical Characteristics

The LP156WH3 requires two power inputs. The first logic is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second backlight is the input about LED BL with LED Driver.

Table 2-1. ELECTRICAL CHARACTERISTICS

Parameter		Combal	Values			11:0:4	Nietee
		Symbol	Min	Тур	Max	Unit	Notes
LOGIC :							
Power Supply Input Voltage		Vcc	3.0	3.3	3.6	V	1
	Red	Icc	-	310	350	mA	2
	Green	Icc	-	305	345	mA	2
Davis Complete and Compart	Blue	Icc	-	290	330	mA	2
Power Supply Input Current	Black	Icc	-	240	275	mA	2
	White	Icc	-	240	275	mA	2
	Mosaic	Icc	-	240	275	mA	2
Power Supply Input Voltage		Vcc	3.0	3.3	3.6	V	1
Power Consumption		Pcc	-	0.8	0.9	W	2
Power Supply Inrush Current		Icc_p	-	-	1500	mA	3
LVDS Impedance		ZLVDS	90	100	110	Ω	4



3. Electrical Specifications

3-1. Electrical Characteristics

The LP156WH3 requires two power inputs. The first logic is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second backlight is the input about LED BL with LED Driver.

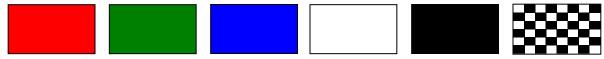
Table 2-2. ELECTRICAL CHARACTERISTICS

D	Symbol	Values			11	
Parameter		Min	Тур	Max	Unit	Notes
BACKLIGHT : (with LED Driver)						
LED Power Input Voltage	VLED	7.0	12.0	21.0	V	5
LED Power Input Current	ILED	-	210	230	mA	6
LED Power Consumption	PLED	-	2.5	2.8	W	6
LED Power Inrush Current	ILED_P	-	-	2000	mA	7
PWM Duty Ratio		5	-	100	%	8
PWM Jitter	-	0	-	0.2	%	9
PWM Impedance	Zpwm	20	40	60	kΩ	
PWM Frequency	Fpwm	200	-	1000	Hz	10
PWM High Level Voltage	V _{PWM_H}	3.0	-	3.6	V	
PWM Low Level Voltage	V _{PWM_L}	0	-	0.3	V	
LED_EN Impedance	Zpwm	20	40	60	kΩ	
LED_EN High Voltage	VLED_EN_H	3.0	-	3.6	V	
LED_EN Low Voltage	VLED_EN_L	0	-	0.3	V	
Life Time		15,000	-	-	Hrs	12

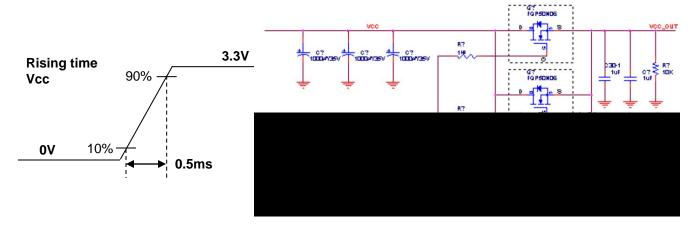


Note)

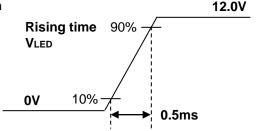
- 1. The measuring position is the connector of LCM and the test conditions are under 25 $^{\circ}$ C, fv = 60Hz, Black pattern.
- 2. The specified lcc current and power consumption are under the Vcc = 3.3V , 25°C , fv = 60Hz condition. (@ R/G/B/White/Black/Mosaic Pattern)



- This Spec. is the max load condition for the cable impedance designing.
- 3. The below figures are the measuring Vcc condition and the Vcc control block LGD used. The Vcc condition is same as the minimum of T1 at Power on sequence.



- 4. This impedance value is needed for proper display and measured form LVDS Tx to the mating connector.
- 5. The measuring position is the connector of LCM and the test conditions are under 25 °C.
- 6. The current and power consumption with LED Driver are under the Vled = 12.0V, 25 ℃, Dimming of Max luminance and White pattern with the normal frame frequency operated(60Hz).
- The below figures are the measuring Vled condition and the Vled control block LGD used.
 VLED control block is same with Vcc control block.



- 8. The operation of LED Driver below minimum dimming ratio may cause flickering or reliability issue.
- 9. If Jitter of PWM is bigger than maximum, it may induce flickering.
- 10. This Spec. is not effective at 100% dimming ratio as an exception because it has DC level equivalent to 0Hz. In spite of acceptable range as defined, the PWM Frequency should be fixed and stable for more consistent brightness control at any specific level desired.
- 11. The life time is determined as the time at which brightness of LCD is 50% compare to that of minimum value specified in table 7. under general user condition.



3-2. Interface Connections

This LCD employs two interface connections, a 40 pin connector used for the module electronics interface and the other connector used for the integral backlight system.

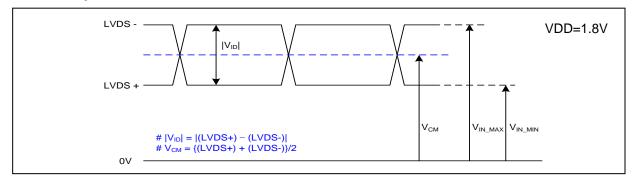
Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	NC	No Connection	[Interface Chip]
2	VCC	LCD Logic and driver power (3.3V Typ.)	1. LCD :
3	vcc	LCD Logic and driver power (3.3V Typ.)	SiW, SW0663(LCD Controller)
4	V EEDID	DDC Power (3.3V)	Including LVDS Receiver.
5	Bist	LCD Panel Self Test Enable	System : SiW LVDSRx or equivalent
6	CIK EEDID	DDC Clock	* Pin to Pin compatible with LVDS
7	DATA EEDID	DDC Data	
8	ORX0-	Negative LVDS differential data input	
9	ORX0+	Positive LVDS differential data input	
10	GND	LCM Ground	[Connector]
11	ORX1-	Negative LVDS differential data input	Hirose KN38-40S-0.5H
12	ORX1+	Positive LVDS differential data input	
13	GND	LCM Ground	
14	ORX2-	Negative LVDS differential data input	[Connector pin arrangement]
15	ORX2+	Positive LVDS differential data input	[
16	GND	LCM Ground	
17	ORXC-	Negative LVDS differential clock input	
18	ORXC+	Positive LVDS differential clock input	1
19	GND	LCM Ground	1
20	NC	No Connection	
21	NC	No Connection	
22	GND	LCM Ground	
23	NC	No Connection	[LCD Module Rear View]
24	NC	No Connection	
25	GND	LCM Ground	
26	NC	No Connection	
27	NC	No Connection	[Note 1]
28	ĞND	LCM Ground	If PWM Duty is changed.
29	NC	No Connection	Brightness can be changed.
30	NC	No Connection	-PWM Duty spec. : 200Hz ~1KHz
31	GND	LCM Ground (LED Backlight Ground)	-PWM High Level : 3.0 ~ 3.6V
32	GND	LCM Ground (LED Backlight Ground)	
33	ĠŇĎ	LCM Ground (LED Backlight Ground)	-PWM Low Level : 0 ~ 0.3V
34	NC	No Connection	
35	PWM	System PWM Signal input for dimming	[Note 2]
36	LED_EN	LED Backlight On/Off	LED EN : 3.0 ~ 3.6V
37	DBC_EN	Dynamic Backlight Control enable(3.0V~3.6V)	LED OFF : 0 ~ 0.3V
38	VLED	LED Backlight Power (7V-21V)	
39	VLED	LED Backlight Power (7V-21V)	
40	VLED	LED Backlight Power (7V-21V)	



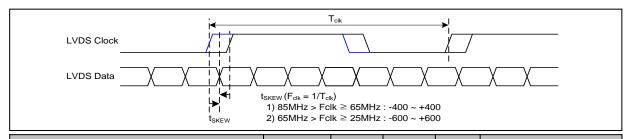
3-3. LVDS Signal Timing Specifications

3-3-1. DC Specification



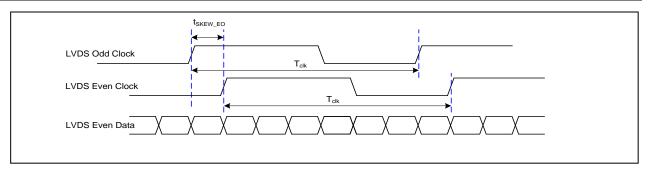
Description	Symbol	Min	Тур	Max	Unit	Notes
LVDS Differential Voltage	V _{ID}	100	-	600	mV	-
LVDS Common mode Voltage	V_{CM}	V _{ID} /2	1.2	VDD- V _{ID} /2	V	-
LVDS Input Voltage Range	V _{IN}	0.3	-	VDD	V	-

3-3-2. AC Specification

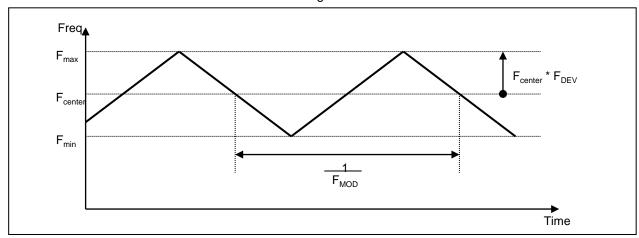


Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skow Margin	t _{SKEW}	- 400	+ 400	ps	85MHz > Fclk ≥ 65MHz
LVDS Clock to Data Skew Margin	t _{SKEW}	- 600	+ 600	ps	65MHz > Fclk ≥ 25MHz
LVDS Clock to Clock Skew Margin (Even to Odd)	t _{SKEW_EO}	- 1/7	+ 1/7	T _{clk}	-
Maximum deviation of input clock frequency during SSC	F _{DEV}	-	± 3	%	-
Maximum modulation frequency of input clock during SSC	F _{MOD}	-	200	KHz	-





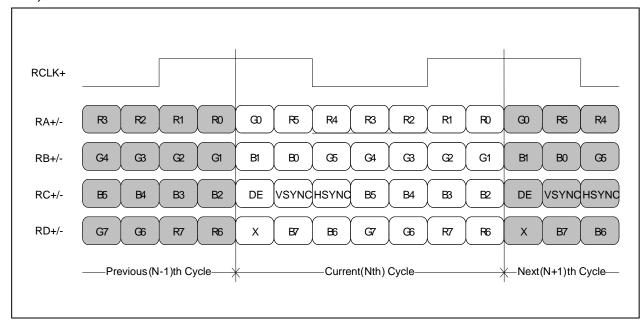
< Clock skew margin between channel >



< Spread Spectrum >

3-3-3. Data Format

1) LVDS 1 Port



< LVDS Data Format >

Condition: VCC =3.3V



Product Specification

3-4. Signal Timing Specifications

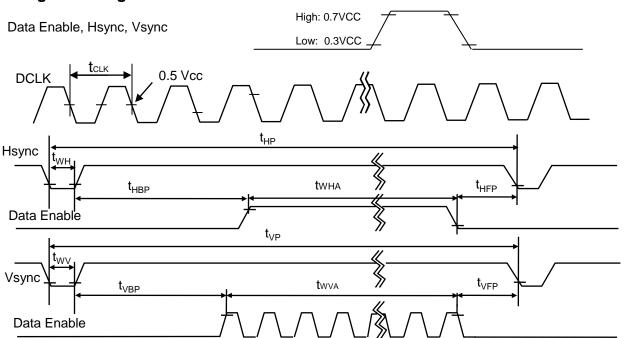
This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 4. TIMING TABLE

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	f _{CLK}		76.75	-	MHz	
	Period	t _{HP}	1596	1618	1642		
Hsync	Width	t _{wH}	32	32	48	tCLK	
	Width-Active	t _{WHA}	1366	1366	1366		
	Period	t _{VP}	780	790	796		
Vsync	Width	t _{WV}	3	5	7	tHP	
	Width-Active	t _{WVA}	768	768	768		
	Horizontal back porch	t _{HBP}	166	172	180	tCLK	
Data	Horizontal front porch	t _{HFP}	32	48	48	ICLK	
Enable	Vertical back porch	t _{VBP}	7	14	16	tHP	
Appendix)	Vertical front porch all reliabilities are specified for tin	ning ^t spe	cification	based on r	efresh rate		. However,

LP156WH3 has a good actual performance even at lower refresh rate (e.g. 40Hz or 50Hz) for power saving Mode, whereas LP156WH3 is secured only for function under lower refresh rate. 60Hz at Normal mode, 50Hz, 40Hz at Power save mode. Don't care Flicker level (power save mode).

3-5. Signal Timing Waveforms





3-6. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 5. COLOR DATA REFERENCE

								Inp	out Co	olor D	ata							
olor			RE	Đ			GREEN				BLUE							
							-											LSB
							 											B 0
																		0
Red	1 	1	1		1	1	0	0	0	0		0	0	0	0		0	0
Green	0				0	0	1		1	. 1 	1	1	0	0		0	0	0
Blue	0	0		0	0	0	0	0	0	0	0	0	1	1		1	1	1
Cyan	0	0	0	0	0	0	1	1	. 1		. 1	1	1	1	.1	1	1	1
Magenta	1	1	1	. 1	1		0	0	0	0	0	0	1	1	1	1	1	
Yellow	1	1	1	. 1	1		1	1	1	1	1	1	0	0	0	0	0	0
White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	0	0	0	0	0	0	0	0	0	0	0	0	1	 1	1	1	 1	1
	Green Blue Cyan Magenta Yellow White RED (00) RED (01) RED (63) GREEN (00) GREEN (01) GREEN (62) GREEN (63) BLUE (00) BLUE (01)	MSE R5 Black 0 Red 1 Green 0 Blue 0 Cyan 0 Magenta 1 Yellow 1 White 1 RED (00) 0 RED (01) 0 RED (62) 1 RED (63) 1 GREEN (00) 0 GREEN (01) 0 GREEN (01) 0 BLUE (00) 0 BLUE (01) 0 BLUE (01) 0	MSB R 5 R 4 R 5 R 4 R 5 R 4 R 5 R 4 R 5 R 4 R 5 R 4 R 5 R 4 R 5 R 4 R 5 R 4 R 6 R 5	Olor MSB R5 R4 R3 Black 0	MSB R5 R4 R3 R2	Olor MSB R5 R4 R3 R2 R1	MSB	Olor MSB	NSB RED LSB MSB R5 R4 R3 R2 R1 R0 G5 G4	Olor MSB RED LSB MSB MSB MSB R5 R4 R3 R2 R1 R0 G5 G4 G3		MSB	Name	Olor MSB Section Se	No No No No No No No No	Olor MSB Section Se	Definition of the properties	Name



3-7. Power Sequence

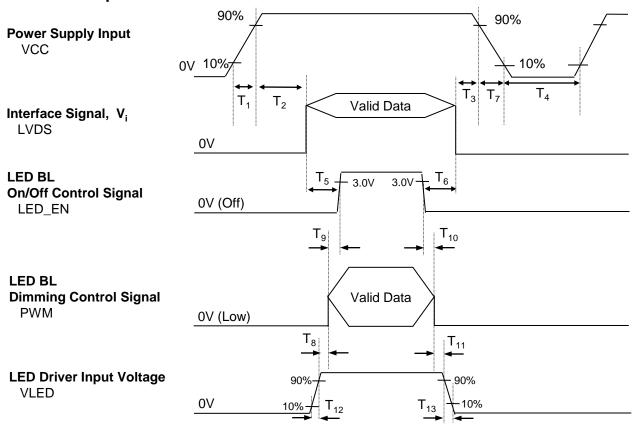


Table 6. POWER SEQUENCE TABLE

Logic		Value		Units			Value		Units	
Parameter	Min.	Тур.	Max.	Units	Parameter	Min.	Тур.	Max.	Units	
T ₁	0.5	-	10	ms	T ₈	10	-	-	ms	
T ₂	0	1	50	ms	T ₉	0	1	-	ms	
T ₃	0	1	50	ms	T ₁₀	0	1	•	ms	
T ₄	400	-	-	ms	T ₁₁	10	-	-	ms	
T ₅	200	-	-	ms	T ₁₂	0.5	-	-	ms	
T ₆	200	-	-	ms	T ₁₃	0	-	5000	ms	
T ₇	3	-	10	ms						

Note)

- 1. Do not insert the mating cable when system turn on.
- 2. Valid Data have to meet "3-3. LVDS Signal Timing Specifications"
- 3. LVDS, LED_EN and PWM need to be on pull-down condition on invalid status.
- 4. LGD recommend the rising sequence of VLED after the Vcc and valid status of LVDS turn on.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 20 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0° .

FIG. 1 presents additional information concerning the measurement equipment and method.

Optical Stage(x,y)

1°

500mm±50mm

FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 7. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, $f_{V}=60Hz$, $f_{CLK}=69.3MHz$

Davamatar	Coursels ad		Values		Lleite	Natas
Parameter	Symbol	Min	Тур	Max	Units	Notes
Contrast Ratio	CR	400	500			1
Surface Luminance, white	L _{WH}	170	200		cd/m ²	2
Luminance Variation	δ _{WHITE (5P)}	-	1.2	1.4		3
	δ _{.WHITE(13P)}	-	1.4	1.6		
Response Time	Tr_{R} Tr_{D}		16	25	ms	4
Color Coordinates						
RED	RX	0.548	0.578	0.608		
	RY	0.314	0.344	0.374	[
GREEN	GX	0.307	0.337	0.367		
	GY	0.541	0.571	0.601	[
BLUE	ВХ	0.129	0.159	0.189	[
	BY	0.090	0.120	0.150	[
WHITE	WX	0.283	0.313	0.343	[
	WY	0.299	0.329	0.359		
Viewing Angle						5
x axis, right(Φ =0°)	Θr	40	-		degree	
x axis, left (Ф=180°)	Θl	40	-	-	degree	
y axis, up (Φ=90°)	Θu	10	-	-	degree	
y axis, down (Φ=270°)	Θd	30	-	Ī .	degree	
Gray Scale]	[6



Note)

1. Contrast Ratio(CR) is defined mathematically as

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$LWH = Average(L1, L2, ... L5)$$

3. The variation in surface luminance, The panel total variation (δ WHITE) is determined by measuring LN at each test position 1 through 13 and then defined as following numerical formula.

For more information see FIG 2.

$$\delta \text{ WHITE (13P)} = \frac{\text{Maximum (L1,L2, ... L13)}}{\text{Minimum (L1,L2, ... L13)}} \delta \text{ WHITE (5P)} = \frac{\text{Maximum(L1,L2, ... L5)}}{\text{Minimum(L1,L2, ... L5)}}$$

- 4. Response time is the time required for the display to transition from white to black (rise time, TrR) and from black to white(Decay Time, TrD). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

*
$$fV = 60Hz$$

Gray Level	Luminance [%] (Typ)
L0	0.15
L7	1.24
L15	4.97
L23	l
L31	20.6
	34.4
L47	53.0
L55	75.7
L63	100

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FIG. 2 Luminance

<Measuring point for Average Luminance & measuring point for Luminance variation>

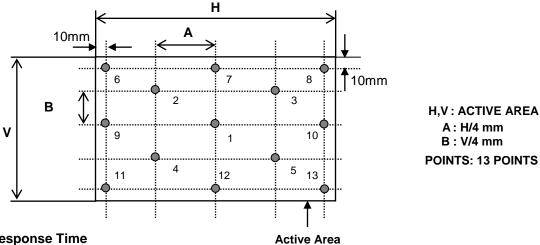
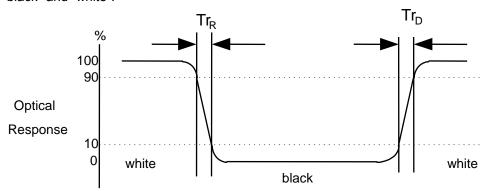
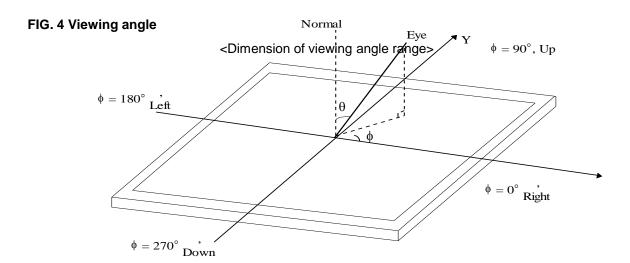


FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".







5. Mechanical Characteristics

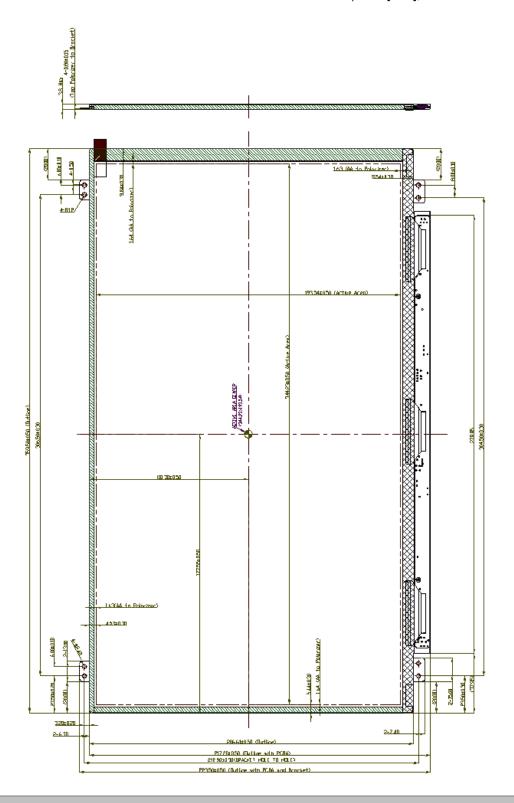
The contents provide general mechanical characteristics for the model LP156WH3. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	359.5 ± 0.5mm				
Outline Dimension	Vertical	217.2 ± 0.5mm				
	Thickness	3.8mm (max)				
Bezel Area	Horizontal	347.5 ± 0.5mm				
	Vertical	196.8 ± 0.5mm				
Active Diepley Area	Horizontal	344.23 mm				
Active Display Area	Vertical	193.54 mm				
Weight	400g (Max.)					
Surface Treatment	Hard Coating(3H), Glare treatment of the front polarizer					



<FRONT VIEW>

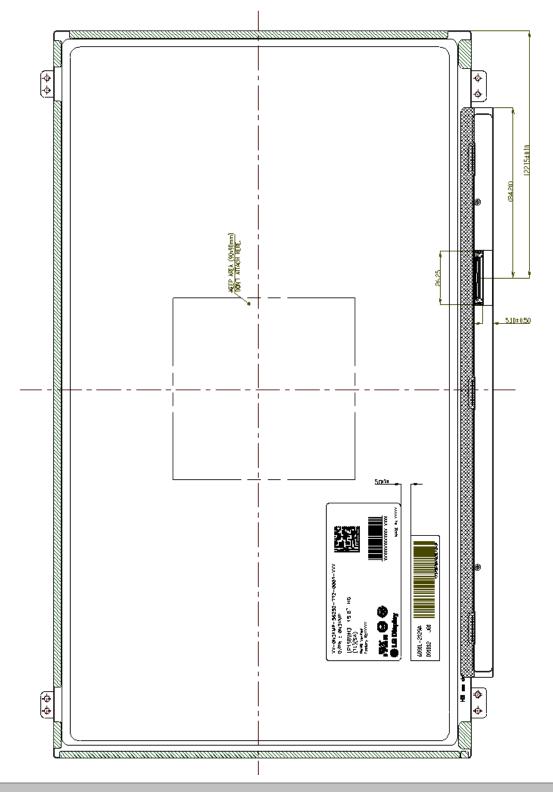
Note) Unit:[mm], General tolerance: ± 0.5mm





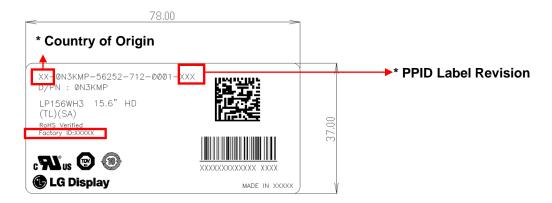
<REAR VIEW>

Note) Unit:[mm], General tolerance: ± 0.5mm





[DETAIL INFORMATION OF PPID LABEL AND REVISION CODE]



* PPID Label Revision :

It is subject to change with Dell event. Please refer to the below table for detail.

Classification	No Change	1st Revision	2nd Revision	•••	9th Revision	•••
SST(WS)	X00	X01	X02	•••	A09	•••
PT(ES)	X10	X11	X12	•••	A19	•••
ST(CS)	X20	X21	X22	•••	A29	•••
XB(MP)	A00	A01	A02		A09	

Country of Origin	Factory ID
CN: China	LGDNJ
KR: Korea	-



6. Reliability

Environment test condition

No.	Test Item	Conditions
1	High temperature storage test	Ta= 60°C, 240h
2	Low temperature storage test	Ta= -20°C, 240h
3	High temperature operation test	Ta= 50°C, 50%RH, 240h
4	Low temperature operation test	Ta= 0°C, 240h
5	Vibration test (non-operating)	Sine wave, 5 ~ 150Hz, 1.5G, 0.37oct/min 3 axis, 30min/axis
6	Shock test (non-operating)	- No functional or cosmetic defects following a shock to all 6 sides delivering at least 180 G in a half sine pulse no longer than 2 ms to the display module - No functional defects following a shock delivering at least 200 g in a half sine pulse no longer than 2 ms to each of 6 sides. Each of the 6 sides will be shock tested with one each display, for a total of 6 displays
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr

[{] Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.



7. International Standards

7-1. Safety

- a) UL 60950-1, Underwriters Laboratories Inc.
 Information Technology Equipment Safety Part 1 : General Requirements.
- b) CAN/CSA C22.2 No.60950-1-07, Canadian Standards Association. Information Technology Equipment - Safety - Part 1 : General Requirements.
- c) EN 60950-1, European Committee for Electrotechnical Standardization (CENELEC). Information Technology Equipment Safety Part 1 : General Requirements.
- d) IEC 60950-1, The International Electrotechnical Commission (IEC).
 Information Technology Equipment Safety Part 1 : General Requirements.

7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics – Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

А	В	С	D	Е	F	G	Н	I	J	К	L	М	1
---	---	---	---	---	---	---	---	---	---	---	---	---	---

A,B,C : SIZE(INCH) D : YEAR

E: MONTH $F \sim M$: SERIAL NO.

Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	Α	В	С	D	Е	F	G	Н	J	K

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 20pcs

b) Box Size: 478mm X 365mm X 328mm



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to to he module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.
- (10) When handling the LCD module, it needs to handle with care not to give mechanical stress to the PCB and Mounting Hole area."

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 mV$ (Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)

 And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

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9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value				
	0		Header	00	(Bin) 00000000				
r	1		Header Header	FF	11111111				
	2	02	Header	FF	11111111				
de	3	03	Header	FF	11111111				
Header	4	04	Header	FF	11111111				
#	5	05	Header	FF	11111111				
	6	06	Header	FF	111111111				
	7	07	Header LCD	00	00000000				
	8	08	ID Manufacture Name LGD ID Manufacture Name	30 E4	00110000 11100100				
	10	09 0A	ID Product Code 03ABh	AB	1010101				
Vendor / Product	11	0B	(Hex. LSB first)	03	00000011				
po	12	0C	ID Serial No Optional ("00h" If not used, Number Only and LSB First)	00	00000000				
Pr	13	0D	ID Serial No Optional ("00h" If not used, Number Only and LSB First)	00	00000000				
	14	0E	ID Serial No Optional ("00h" If not used, Number Only and LSB First)	00	00000000				
op	15	0F							
len/	16 17	10	Week of Manufacture - Optinal 00 weeks	00	00000000				
	18	11 12	Year of Manufacture 2012 years EDID structure version # = 1	16 01	00010110				
	19	13	EDID revision # = 4 Video input Definition = Input is a Digital Video signal Interface, Colo Bit Depth: 6 Bits per Primary Color,	04	00000100				
	20	14	Digital Video Interface Standard Supported: Digital Interface is not defined	90	10010000				
	21	15	Horizontal Screen Size (Rounded cm) = 34 cm	22	00100010				
ay a	22	16	Vertical Screen Size (Rounded cm) = 19 cm	13	00010011				
lds	23	17	Display Transfer Characteristic (Gamma) = (gamma*100)-100 = Example:(2.2*100)-100=120 = 2.2 Gamma	78	01111000				
Display	24	18	Feature Support [Display Power Management(DPM): Standby Mode is not supported, Suspend Mode is not supported, Active Off = Very Low Power is not supported, Supportted Color Encoding Formats: RGB 4:4:4 & YCrCb 4:4:4, Other Feature Support Flags: No_sRGB, Preferred Timing Mode, No_Display is continuous frequency (Multi-mode_Base EDID and Extension Block).]	0A	00001010				
	25	19	Red/Green Low Bits (RxRy/GxGy)	05	00000101				
	26	1A	Blue/White Low Bits (BxBy/WxWy)	F5	11110101				
Vendor / Product	27	1B	Red X Rx = 0.578	94	10010100				
odi	28	1C	Red Y Ry = 0.344	58	01011000				
Pr	29	1D	Green X $Gx = 0.337$	56	01010110				
<u>.</u>	30	1E	Green Y $Gy = 0.571$	92	10010010				
ndo	31	1F	Blue X $Bx = 0.159$	28	00101000				
/e1	32	20	Blue Y By = 0.120	1 E	00011110				
	33	21	White X $Wx = 0.313$	50	01010000				
	34	22	White Y $Wy = 0.329$	54	01010100				
pa	35	23	Established timing 1 (Optional_00h if not used)	00	00000000				
Established	36	24	Established timing 2 (Optional_00h if not used)	00	00000000				
Esta	37	25	Manufacturer's timings (Optional_00h if not used)	00	00000000				
	38	26	Standard timing ID1 (Optional_01h if not used)	01	00000001				
	39	27	Standard timing ID1 (Optional_01h if not used)	01	00000001				
	40		Standard timing ID2 (Optional_01h if not used)	01	00000001				
Q	41	29	Standard timing ID2 (Optional_01h if not used)	01	00000001				
g I	42	2A	Standard timing ID3 (Optional_01h if not used) Standard timing ID3 (Optional_01h if not used)	01	00000001				
ing	43	2B 2C	Standard timing ID3 (Optional_01h if not used) Standard timing ID4 (Optional_01h if not used)	01 01	00000001				
im	45	2D	Standard timing ID4 (Optional_Offi in not used) Standard timing ID4 (Optional_Offi in not used)	01	00000001				
d 7	46	2E	Standard timing ID5 (Optional_01h if not used)	01	00000001				
ar	47	2F	Standard timing ID5 (Optional_01h if not used)	01	00000001				
Standard Timing ID	48	30	Standard timing ID6 (Optional_01h if not used)	01	00000001				
Sta	49	31	Standard timing ID6 (Optional_01h if not used)	01	00000001				
	50 51	32	Standard timing ID7 (Optional_01h if not used) Standard timing ID7 (Optional_01h if not used)	01 01	00000001				
-	51	33 34	Standard timing ID7 (Optional_01h if not used) Standard timing ID8 (Optional_01h if not used)	01	00000001				
	53	35	Standard timing ID8 (Optional_01h if not used)	01	00000001				
	55			VI					



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	54		Pixel Clock/10,000 (LSB) 76.75 MHz @ 60Hz	FB	11111011
	55	37	Pixel Clock/10,000 (MSB)	1D	00011101
	56	38	Horizontal Active (HA) (lower 8 bits) 1366 Pixels	56	01010110
	57	39	Horizontal Blanking (HB) (lower 8 bits) 252 Pixels	FC	11111100
	58	3A	Horizontal Active / Horizontal Blanking(HA HB) (upper 4:4bits)	50	01010000
I #	59	3B	Vertical Avtive (VA) 768 Lines	00	00000000
] . 16	60	3C	Vertical Blanking (VB) (DE Blanking typ.for DE only panels) 22 Lines	16	00010110
ipt	61	3D	Vertical Active / Vertical Blanking (VA VB) (upper 4:4bits)	30	00110000
Timing Descriptor #1	62	3E	Horizontal Front Porch in pixels (HF) (lower 8 bits)48 Pixels	30	00110000
De	63	3F	Horizontal Sync Pulse Width in pixels (HS) (lower 8 bits) 32 Pixels	20	00100000
0.0	64	40	Vertical Front Porch in lines (VF) (lower 4 bits): Vertical Sync Pluse Width in lines (VS) (lower 4 bits)	35	00110101
ni	65	41	Horizontal Front Porch/ Sync Pulse Width/ Vertical Front Porch/ Sync Pulse Width (upper 2bits)	00	00000000
Ti.	66	42	Horizontal Vedio Image Size (mm) (lower 8 bits) 344 mm	58	01011000
	67	43	Vertical Vedio Image Size (mm) (lower 8 bits) 194 mm	C2	11000010
	68	44	Horizontal Image Size / Vertical Image Size (upper 4 bits)	10	00010000
	69	45	Horizontal Border = 0 (Zero for Notebook LCD)	00	00000000
	70	46	Vertical Border = 0 (Zero for Notebook LCD)	00	00000000
	71	47	Non-Interlace, Normal display, no stereo, Digital Separate [Vsync_NEG, Hsync_POS (outside of V-sync)]	1A	00011010
	72	48	Pixel Clock/10,000 (LSB) 51.16 MHz @ 40Hz	FC	11111100
	73	49	Pixel Clock/10,000 (MSB)	13	00010011
	74	4A	Horizontal Active (HA) (lower 8 bits) 1366 Pixels	56	01010110
	75	4B	Horizontal Blanking (HB) (lower 8 bits) 252 Pixels	FC	11111100
	76	4C	Horizontal Active / Horizontal Blanking(HA HB) (upper 4:4bits)	50	01010000
#2	77	4D	Vertical Avtive (VA) 768 Lines	00	00000000
5	78	4E	Vertical Blanking (VB) (DE Blanking typ.for DE only panels) 22 Lines	16	00010110
ipt	79	4F	Vertical Active / Vertical Blanking (VA VB) (upper 4:4bits)	30	00110000
S S	80	50	Horizontal Front Porch in pixels (HF) (lower 8 bits)48 Pixels	30	00110000
Timing Descriptor #2	81	51	Horizontal Sync Pulse Width in pixels (HS) (lower 8 bits) 32 Pixels	20	00100000
50	82	52	Vertical Front Porch in lines (VF) (lower 4 bits): Vertical Sync Pluse Width in lines (VS) (lower 4 bits)	35	00110101
ni.	83	53	Horizontal Front Porch/ Sync Pulse Width/ Vertical Front Porch/ Sync Pulse Width (upper 2bits)	00	00000000
	84	54	Horizontal Vedio Image Size (mm) (lower 8 bits) 344 mm	58	01011000
	85	55	Vertical Vedio Image Size (mm) (lower 8 bits) 194 mm	C2	11000010
	86	56	Horizontal Image Size / Vertical Image Size (upper 4 bits)	10	00010000
	87	57	Horizontal Border = 0 (Zero for Notebook LCD)	00	00000000
	88	58	Vertical Border = 0 (Zero for Notebook LCD)	00	00000000
	89	59	Non-Interlace, Normal display, no stereo, Digital Separate [Vsync_NEG, Hsync_POS (outside of V-sync)]	1A	00011010
	90	5A	Flag	00	00000000
	91	5B	Flag	00	00000000
	92	5C	Flag	00	00000000
	93	5D	Data Type Tag: Alphanumeric Data String (ASCII String)	FE	11111110
	94	5E	Flag	00	00000000
#3	95	5F	Dell P/N 1st Character = N	4E	01001110
Timing Descriptor #3	96	60	Dell P/N 2nd Character = 3	33	00110011
Ţ.	97	61	Dell P/N 3rd Character = K	4B	01001011
sci	98	62	Dell P/N 4th Character = M	4D	01001101
De	99	63	Dell P/N 5th Character = P	50	01010000
811	100	64	EDID Revision Build Name = MP(X-Build), Revision # = A00	80	10000000
mi	101	65	Manufacturer P/N = 1	31	00110001
Ţ	102	66	Manufacturer $P/N = 5$	35	00110101
	103	67	Manufacturer P/N = 6	36	00110110
	104	68	Manufacturer P/N = W	57	01010111
	105	69	Manufacturer P/N = H	48	01001000
	106	6A	Manufacturer $P/N = 3$	33	00110011
	107	6B	Manufacturer P/N (If < 13 char, then terminate with ASC Π code 0Ah,set remaining char = 20h)	0A	00001010



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	108	6C	Flag	00	00000000
	109	6D	Flag	00	00000000
	110	6E	Flag	00	00000000
	111	6F	Data Type Tag: Descriptor Defined by manufacturer	00	00000000
	112	70	Flag	00	00000000
	113	71	Color Management [No +2 FRC Support, True Color Depth : 6 bit]	00	00000000
#	114	72	Panel Type [WLED] , Configuration [Single light bar], Number Lamp or LED Light Bar [one]	41	01000001
Timing Descriptor #4	115	73	Frame Rate Details [Minimum Frame Rate : 40Hz, Maximum Frame Rate : 65Hz , Tcon provides native Intel DRRS / sDRRS support]	31	00110001
cri	116	74	Controller Interface and Maximum Luminance [PWM type, 200 nit]	94	10010100
es es	117	75	Front Surface / Polarizer [Anti-Glare, No Transflective] , Pixel Structure [RGB v-stripe]	00	00000000
g L	118	76	Multi-Media Features [Color Management : NTSC, Dynamic Backlight Control : Type 1]	10	00010000
·ii	119	77	Multi-Media Features [Motion Blur : No support , Active Gamma Control : No support]	00	00000000
بق	120	78	Special Features [Wireless Enhancement Hardware: No support, In-Cell Scanner: No support]	00	00000000
	121	79	Special Features [Number of LVDS channels or eDP lanes : one , Overdrive : No ,Interface : LVDS , In-Cell Touch Support : No]	01	00000001
	122	7A	Special Features [BIST Support : yes , Electronic Privacy : No electronic privacy hardware support , 3-D Support : No]	01	00000001
	123	7B	(If<13 char> 0Ah, then terminate with ASC Π code 0Ah,set remaining char = 20h)	0A	00001010
	124	7C	(If<13 char> 0Ah, then terminate with ASC Π code 0Ah,set remaining char = 20h)	20	00100000
	125	7D	(If<13 char> 0Ah, then terminate with ASC \coprod code 0Ah,set remaining char = 20h)	20	00100000
Checksum	126	7 E	Extension flag (# of optional 128 panel ID extension block to follow, Typ = 0)	00	00000000
Checi	127	7 F	Check Sum (The 1-byte sum of all 128 bytes in this panel ID block shall = 0)	D0	11010000